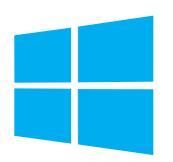
# Operating System Notes







# What is Operating system? explain the types of Operating system

- Operating system is a system software that manages the computer hardware
- It acts as intermediary between computer user and computer hardware
- e.g., windows, Linux, android, mac OS
- Types of OS
  - <u>Batch OS</u> -this type of OS does not interact with computer directly; it allows multiple users to use it at the same time ,E.g., of batch OS : payroll system, bank statements, etc.
  - <u>Time sharing</u> each process is given some time to be executed, the time given to each process to execute is called as quantum, after this time is over the OS switched over to next process
  - Real Time Sharing these OS serves real time system, the time required to process is very small. E.g., air bags, missile system There are two types of Real time OS: hard(where time constraints are very strict) & soft(where time constraints are less strict)
  - Multi programming there are one or more program loaded in the main memory which are ready to be executed, Here only one program can use the CPU at a time, while others have to wait
- Goals of OS
  - Convenience
  - Efficient
  - Both

# Difference Between Multiprogramming and Multitasking

| Multiprogramming   | Multitasking                              |
|--|---|
| Multiprogramming is non-preemptive   | Multitasking is Preemptive                |
| CPU is allocated to the Process until the process is terminated or goes in waiting state | CPU is allocated for a time quantum       |
| Here Execution of Program takes more time  | Here Execution of program takes less time |
| CPU Idleness is more   | CPU idleness is less                      |
| Waiting time and Response time is<br>More  | Waiting time and Response time is Less    |
| P1<br>5hr  | P1 2hr                                    |
| P2 — 3hr — CPU  2hr  | P2 — 2hr — CPU  2hr                       |

# What is System Call? Give some examples of System calls

- System call provides interface between a process and the operating system
- System call is mostly in the form of assembly language
- Types of system calls
  - <u>Process control</u>: end, abort, execute, create process, load process, terminate process
  - o File management : create file, delete file, open, close, read, write
  - o <u>Device management</u>: request/release device, read/write
  - <u>Information management</u>: get/set time, set date, get/set system data
  - <u>Communication management</u>: create/delete communication, send/receive messages
  - <u>Protection</u>: provides mechanism for controlling access to the resources

# Explain the Process of Booting and Re-Booting

#### **Booting**

- Starting of the computer or an embedded system is called as booting
- · Steps in Booting
  - Switching on power supply
  - Loading Operating system in the computer's main memory
  - Keeping all application in the state of readiness in case user needs
- First program of set of instructions what execute/run when the computer is booted is called as BIOS (Basic Input Output System)
- BIOS is a firmware

#### Rebooting

- Restarting the system when it is already running is called as rebooting
- · When do we need rebooting
  - When software or hardware has been installed
  - When system is unusually slow
- Rebooting is faster than booting

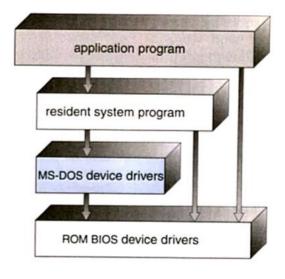
# ?

### **Explain the Structure of OS**

#### **Simple OS structure**

 Does not have a well-defined structure

• Ex: MS-DOS



#### **Layered Approach**

- Here the OS is divided into layers each on top of each other
- Bottom layer (layer 0) is hardware and topmost layer (layer n) is the user interface

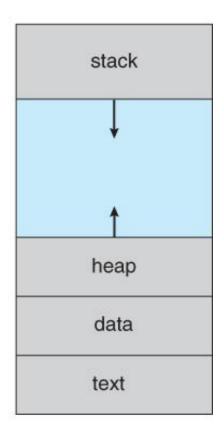
| Layer 5 | User Program          |
|---------|-----------------------|
| Layer 4 | I/O Management        |
| Layer 3 | Process Communication |
| Layer 2 | Memory Management     |
| Layer 1 | CPU Scheduling        |
| Layer 0 | Hardware              |

### Explain What is Process Management

- Process management includes creating, running, terminating and assigning process to the CPU
- Process is program in execution
- Process is generated by OS and program is written by user
- When executable files get loaded in main memory and when it starts executing instructions one by one using process counter it is called as process
- We can run executable file by simply double clicking it or by running it on command prompt by inputting its name

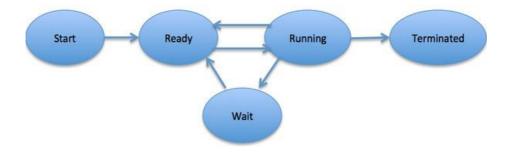
### Structure of Process in Memory

- Stack is used to store temporary values
- These temporary values can be parameters or return values of functions or local variables
- Data section store global variables
- Heap section is used for dynamic memory allocation
- Text section carries the program code



### Explain Process State with Diagram

- New or start : the process is being created
- Running : the process is currently running in the CPU
- Waiting: the process is ready to execute but have to wait till the CPU is free or wait for I/O devices to be free
- Ready: the process is in main memory and has to wait to be assigned to the CPU
- Terminated: the process has been executed
- <u>Blocked</u>: if the process waits for to long then there is an additional state where it goes into deadlock and will never be able to execute again



# Explain Process Control Block

- PCB is used to track the process execution status
- Process state : new, running, ready, waiting, terminated
- process number (ID): it is the unique ID of the process
- program counter (PC): it is used to indicate the address of next instruction which is going to be executed
- CPU register : accumulator, stack pointers, general purpose registers, index registers

| Process state (PS)   |
|----------------------|
| Process number (ID)  |
| Program counter (PC) |
| Registers            |

# What is Thread? Explain Benefits of Multi-Threading

- Thread is the basic unit of CPU utilization
- Thread is associated with thread ID, program counter, register set and stack
- Generally, a normal process is single threaded but processes can be multi-threaded
- If the system is multi-threaded it is very powerful and can perform multiple tasks at a time

#### **Benefits of Multiprogramming**

- Responsiveness: it allows the process to execute or run continuously even If the part of it is blocked
- Resource sharing: thread belong to a particular process they can also use the resources allocated to the process, so many threads can share resources of the process they belong to
- Economy: Process creation is costly due to memory allocation and resource allocation, as thread shares all resources from its process it is economical
- Utilization of multiprocessor architecture

# What is CPU Scheduling? or Explain the Criteria of CPU Scheduling? or State the parameters of CPU Scheduling

- CPU scheduling is the process of assigning different jobs to the CPU by creating a scheduling using scientific sequence
- Parameters used in scheduling criteria

CPU utilization

Throughput

Turnaround time

Waiting time

Response Time

#### CPU utilization

It is important to keep CPU busy all the time so it gets fully utilized, CPU utilization is measured in percentage In real time CPU utilization is in the range of 40% to 90%

- <u>Throughput</u>: Number of processes completed in a unit of time is called as throughput
- <u>Turnaround time</u>: It is the time required for the process to be executed
- <u>Waiting time</u>: The time spend in the waiting queue is known as waiting time
- Response time: The time between actual submission and first response

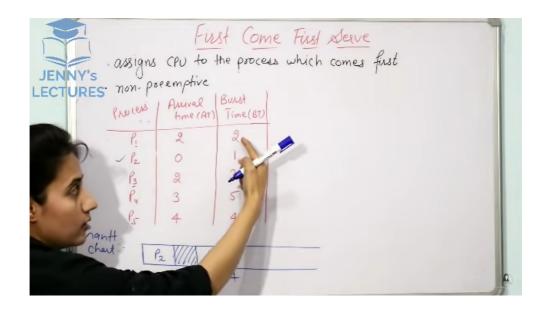
# Difference Between Preemptive and Non-Preemptive

| Preemptive   | Non-Preemptive   |
|--|--|
| CPU is allocated for a time quantum                                | CPU is allocated to the Process until the process is terminated or goes in waiting state |
| The Processes in Preemptive<br>Scheduling can be interrupted       | Processes cannot be interrupted  |
| CPU utilization is higher as compared to non-preemptive scheduling | CPU utilization is less as compared to<br>Preemptive Scheduling                          |
| Waiting time and Response time is Less                             | Waiting time and Response time is<br>More  |
| E.g., Windows 95   | E.g., Windows 3  |

### **CPU** Scheduling Algorithms

Check all the CPU Scheduling Algorithms by clicking on the Youtube links provided below

https://www.youtube.com/@JennyslecturesCSIT



- FCFS Algorithm
- SJF Algorithm
- SRTF Algorithm
- RR Algorithm

**NOTE**: CPU Scheduling Hand Written Algorithms will be provided soon

### **First Come First Serve**

|      | PAGE NO / DATE / / /   |
|------|--|
| *    | Algonithm  |
| 1]   | First Come First Serve   |
|      | · Non - Paeemptive   |
|      | A P A COLUMN THE WAY A PROPERTY OF THE PARTY |
|      | Process Annival Burst CT TAT WT RT   |
|      | No Time Time   |
|      | and an analysis of the part ? (2)  |
| art  | PI 0 2 2 2 0 0   |
| 1316 | P2 11 2 4 3 1 1  |
|      | P3 5 3 8 3 0 0   |
|      | P4 6 4 12 6 2 2  |
|      |  |
| 1000 | Grant chart  |
| 10   | P1 P2 ///// P3 P4  |
|      | 0 1 2+2 4 5+3 8+4  |
|      | LP2  |
| 3/6  | Treeogy (Reoay) (1)  |
| LOOK | AT = CT - AT   |
|      | TATE BT HE SHARE THE STATE OF T |
|      | them we are the party place of a state of the state of th |
| IF   | Alapaithm is also a  |
|      | Algorithm is Non-Preemptive  |
|      | RT = WT  |
| 0.   |  |
| 70   | erage TAT = 2+3+3+6 = 14 = 3.5   |
|      | 4 4  |
|      |  |
|      |  |
|      |  |
|      |  |

### First Come First Serve (Page 2)

| Average | WT =        | 0+1-  | 40+2         | = 3     | = 0.45        |         |
|---------|-------------|-------|--------------|---------|---------------|---------|
|         |             |       |              |         | 9 40 100      |         |
| Average | CT =        | 2+4+  | 8+12         |         |               |         |
|         | FAT         | 12.11 | 4 11         | 4       | 1971 40       | aran3   |
|         |             |       | 3015         | - smile |               |         |
| 0       |             | 21    |              |         |               |         |
| £.9.    | -1211       | 10 3  | 1 8 1        | 10 100  | 140.00        | 100.10  |
| Process | АТ          | Вт    | СТ           | TAT     | WI            | I RT    |
| No      | 0.1         | 641   | Щ            | (2      |               | JP -    |
|         |             |       |              |         |               |         |
| P       | 2           | 2     | 13           | 11.     | 9             | 9       |
| P2      | 5           | 6     | 19           | 14      | 8             | - 8     |
| P3      | 0           | -4    | 4            | 4       | 0             | 0       |
| P4      | 0           | 7     | - 11         | 17-     | 4             | 4       |
| P 5     | 7           | 4     | 2.3.         | 16      | 12            | 12.     |
| 30.33   | 18295       | CLE   | 2+32         | 21 年 9  | WE 35         | 30328   |
|         | 4-7         |       | H            |         |               |         |
| P3,     |             | 04    | PL           |         | 2             | Ps      |
| D4 1    | 4+7 1<br>P2 | 4     | +2           | 13+6    | 1) 19+        | 4 2 2 3 |
| (Ready) |             | eody) | P3<br>(Ready | 1       | 0 10 0        |         |
|         | peady)      |       | CRESOY       | 40 2 3  | T Last 19 and | 101100  |
|         |             |       | 120          |         |               |         |
|         |             |       |              | 122 610 |               |         |

### **Shortest Job First (SJF)**

| 2] | Phontest ]   | ob Finsl | (5JF)            | 1.19.1 | THE    | 14-14-14 |    |
|----|--------------|----------|------------------|--------|--------|----------|----|
|    | · Non - Pres |          |                  |        |        | 1.44     |    |
|    | Process      | Aggivol  | Bunst            | СТ     | TAT    | WT       | RT |
|    | No           | Time     | Time             |        |        |          | 4  |
|    | PI           | 1        | 3                | 6      | 5      | 2        | 2  |
|    | Po           | 2        | 4                | 10     | 8      | 4        | 4  |
|    | 11P3 TILL    | PRIM     | 2                | -3     | 2      | 000      | 0  |
|    | Pu           | 4        | 4                | 14     | 10     | 161      | 6  |
|    | Carrier on   | Total I  | -58              | 0      | 1000   |          |    |
|    | Groot chart  | F- P     | New York         | P2     | 1 1 p  | 4        |    |
| 9  | 1+2          |          | 6+4              | 1.0    | 10+4   | 01       | 4  |
|    | PULPURN      |          |                  | 33     | 1 19 1 | 7 4      |    |
|    | Avenoge TA   | T = 5    | +8+2+            | 10     | = 25   | - 6.21   | 5  |
|    |              | 1 40 60  | 200              | -10    |        |          |    |
| F  | average CT   | = 6+     | 10+3+            | 14     | = 33   | = 8.     | 25 |
|    |              | 50000    | 9 4 9 1          |        |        |          |    |
| A  | lverroge W   | T = 2+0  | 1+0+1            | 5 =    | 10     | 0        |    |
|    | J            |          | 4                |        | 12     | = 3.     |    |
|    |              |          | P. P. Commission |        | -      |          |    |

### **Shortest Remaining Time First (SRTF)**

|   | · Preem  | ptive   |           |        |        | 2011    | 09.8         |   |
|---|----------|---------|-----------|--------|--------|---------|--------------|---|
|   |          | IT      | Balleties | Anna A | 120101 | I Lylin | Pape         |   |
|   |          | Angival |           |        |        |         | RT           | 4 |
|   | No       | time    | time      |        | -      |         | ++           | 4 |
|   | 3111     | 2000    | 10.7      | -0     | 19     |         | 11           | 4 |
|   | PI       | 0101    | \$432°    | 9      | 89     | 4       | 0            | 4 |
|   | P2       | 0111    | 3210      | 40     | 3 8    | 0       | 0            | 4 |
|   | P3       | W 2 8   | 4321      | 13     | 1155   | 4       | 404          | 4 |
|   | P4       | 4       | 1         | 5      |        | 0       | 0            |   |
| - | RT = fin | ing rad | +4+13+    | 3 110  | 119    | 12 /    | Lizery)      |   |
|   | Avenage  | TAT = Q | +3+11+    | +01+3  | 24     | = 0     | 6<br>10 14 A |   |
|   | Average  | WT = 4, | 5 4 5     | D =    | 11 4 + | = Q.    |              |   |
| 1 | Avenage  | RT = Q  | +0+7+     | 0 =    | 7      | =   .=  | 15           |   |

### **Priority Scheduling**

|  |                                 |                             |                                     |                                  |                              | 14,11                | 1 10 |
|--|---------------------------------|-----------------------------|-------------------------------------|----------------------------------|------------------------------|----------------------|------|
|  |                                 | Lina                        | Algor                               | ithm                             | 18 10                        | hooly                | 10   |
| Pasonit  | y Sche                          | auting                      |                                     |                                  |                              |                      |      |
| -  | - L'UP                          | Anaivol                     | Boust                               | CT                               | TAT                          | WT                   | RT   |
| 10 20 4  | P1<br>P2 P3 P                   | 2                           | 9 5 3 4 9 to 32 4 to                | 15                               | 15                           | 6                    | 0 0  |
| The same of the sa |                                 |                             |                                     |                                  |                              |                      |      |
|  |                                 |                             |                                     |                                  |                              |                      |      |
| Higher   | y the r                         | 10. high                    | ien po                              | ripait                           | y kno                        | 2 toni               |      |
| Grant cha  | INT<br>P3 P                     | 3 P4 1                      | pu pu                               | Pul                              | P2 P2                        | P2 P10               | ,    |
| Grant cho  | INT<br>P3 P                     | 3 P4 1                      | Pu Pu                               | Pu  <br>8 1+F                    | P2 P2                        | P2 P1                | 5    |
| Hant cho<br>P1 P2<br>O+1 1+1   | 174<br>  P3   P<br>2+1 3+1      | 13 P4 1                     | Pu Pu                               | P4  <br>  8   + F                | P2 P2                        | P2 P10               | 5    |
| Grant cho<br>Pr P2<br>O+1 1+1<br>Average   | P3 P 2+1 3+1                    | 3 PH 5                      | Pu Pu<br>+1 6+1<br>+1 4 8           | Pu  <br>  7+1 8<br>  = 38<br>  4 | P2 P2<br>3+1 9+1 10<br>= 9.5 | P2/P10<br>0+1 11+4   | 15   |
| Grant cho<br>Pr P2<br>O+1 1+1<br>Average   | P3 P 2+1 3+1                    | 3 PH 5                      | Pu Pu<br>+1 6+1<br>+1 4 8           | Pu  <br>  7+1 8<br>  = 38<br>  4 | P2 P2<br>3+1 9+1 10<br>= 9.5 | P2/P10<br>0+1 11+4   | 15   |
| Hant che<br>Pi P2<br>O+1 1+1<br>Average  | P3 P 2+1 3+1    8   CT   =      | 15 + 11 +<br>4+1 5          | P4 P4<br>+1 6+1<br>+1 6+1<br>+1 4 8 | P4   7+1 8<br>= 38<br>4<br>= 3   | P2 P2<br>3+1 9+1 10<br>= 9.5 | P2   P1g<br>p+1 11+4 | 15   |
| Average  Average   | P3 P 2+1 3+1  18  CT 1=  WT = 1 | 15 + 10 +<br>4<br>0 + 6 + 0 | P4 P4<br>+1 6+1<br>+1 6+1<br>+1 2+4 | = 38<br>= 38<br>= 3              | P2 P2<br>3+1 9+1 10<br>= 9.5 | P2   P1g<br>p+1 11+4 | 15   |

### **Round Robin Agorithm**

| 7 5] | Round Robin Scheduling Algorithm (Time quontum decided)  |
|------|--|
|      | Process Aggival Bugst CT TAT WT RT No Time Time  |
|      | P1 0 5 13 13 8 0  P2 1 3 6 8 7 4 1  P3 2 7 5 3 2 2  P4 3 2 0 10 7 5 5  P5 4 3 0 14 10 7 6  |
|      | Ready Queve<br>PI P2 P3 P4 P5 P1 P5  |
|      | (mont chart = 32 - P4 P5 P1 P5 |
|      | Average CT = 13 + 8 + 5 + 10 + 4 = 40 = 8  5  5  |
|      | Average TAT = 13+3+3+1+10 = 40 = 8 5 5   |
| 1    | Average NT = 8+4+2+5+7 = 26 = 5.2 5 5  |
|      | Avenage RT = 0+1+2+5+6 = 14 = 2.8 5 5  |

# What does memory management do? and Explain Logical and Physical address

- Memory management in OS keeps track of the memory which is currently used and by whom it is used
- These facilities to know how much memory is fetch or occupied by the process
- Decides which process should be loaded in the main memory when space is empty
- Logical address: the address produced by the CPU is called as the logical address
- Physical address: the address loaded in the memory register is called as physical address
- MMU (Memory management unit): converts the logical address into physical address

# Explain what is loading? also explain the types of loading

- Creation of program requires loading the program in main memory
- The work of loader is to place load module in main memory
- There are 3 types of Loading, Absolute, relocatable and dynamic

#### Absolute

 It loads the load module at a specific memory location, this location is usually absolute and does not change when the program is loaded in memory

#### Relocatable

 The load module is placed anywhere is the main memory, this is possible if the assembler or compiler produce the address that are relative to some point

#### • Dynamic

- Loading the modules into main memory on demand is called as dynamic loading, the program is loaded into main memory and is executed
- When this program needs to call modules or another program it will check if it is loaded or not, If the program is not loaded it is loaded

### What is Swapping?

- in the operating system is a memory management scheme that temporarily swaps out an idle or blocked process from the main memory to secondary memory and vice versa
- Swapping ensures proper memory utilization and memory availability for those processes which are ready to be executed.
- Swap-out is a method of removing a process from RAM and adding it to the hard disk.
- Swap-in is a method of removing a program from a hard disk and putting it back into the main memory or RAM.
- · Advantages of swapping
  - multiple process can run by CPU using swapping
  - o process do not have to wait long for execution
  - Improve the degree of multi-programming.

### **Explain Memory Partitioning and Its types**

- Memory partitioning is a technique that is used to divide memory into several small parts known as partitions
- Advantages of memory partitioning
  - Useful for multiprogramming environment
  - Easy to fetch data from partitions
  - Easy to access partitions

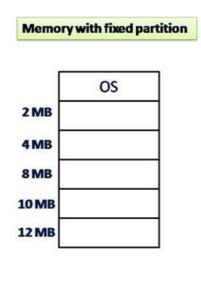
#### Static memory partitioning

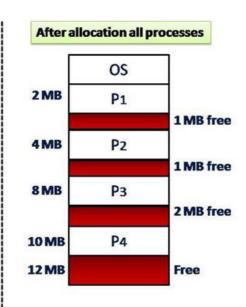
- This method is also called as fixed partitioning
- The partitions are of fixed size
- If the process assigned to the memory partition is lesser than the fixed size then the remaining space cannot be used by any other process, This is called as internal fragmentation

#### **Dynamic memory Partitioning**

- The partition is done as per the requirement of the process length or process size
- · Maximum memory gets utilized
- When some process gets terminated its space is free but it might remain used by bigger process
- This is called as external fragmentation

#### Static memory partitioning





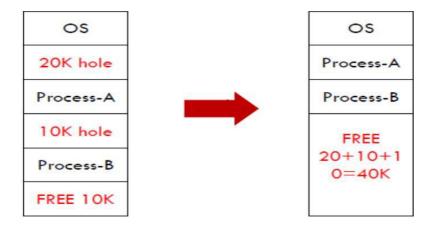
#### **Dynamic memory Partitioning**

Process P2
Process P3
Process P4

| Operating<br>System  |
|----------------------|
| Process P1<br>(5 MB) |
| Process P2<br>(2 MB) |
| Process P3<br>(3 MB) |
| Process P4<br>(4 MB) |

# Explain Compaction, Paging and Segmentation

- Compaction is a technique to overcome external fragmentation in dynamic memory partitioning
- All the free space is compacted together to form a new block or partition
- Compaction is a complex process



- Paging is another method which we can use instead of compaction to overcome external fragmentation in dynamic memory allocation
- In paging physical memory is broken into fixed size blocks called as frames
- - And logical memory is broken into fixed size blocks called as pages
- - Size of one page is equal to the size of one frame
- In Operating System, Segmentation is a memory management technique in which the memory is divided into the variable size parts.
   Each part is known as a segment which can be allocated to a process.
- Segment table is formed which contains segment number and it's offset

### What is Deadlock?

- In multiprogramming environment process compete for physical as well as for logical recourses
- If a process P1 request for resource which is not available (held by some other process), P1 enters wait state
- It may happen that the wait state might never change because the resource requested by P1 is occupied by some other process
- This is called as Deadlock
- Ex: let's assume two processes P1 and P2, P1 is holding tape drive and P2 is holding printer, deadlock can occur if P1 request for printer and P2 request for tape drive



#### **List the Conditions for Deadlock**

#### Mutual exclusion

- One process can hold only one resource at a time
- Let's say if P1 is holding onto a resource and if another process P2 request for the same resource at the same time then the process P2 has to wait until P1 releases this resource

#### Hold and wait

 P1 is hold a resource and is requesting for another resource which is held by another process (P2)

#### No preemption

- Resources cannot be taken away forcefully by any process
- The process holding it should release it in order that other process can use it

#### Circular wait

 P1 is requesting for a resource which is held by P2 and P2 is requesting for resource which is held by P1



#### **List the factors to Prevent Deadlock**

- Elimination of Mutual exclusion
  - Deadlock can be prevented by eliminating mutual exclusion
- Eliminate hold and wait condition
  - We should ensure that the resource requested by the process is not held by any other process
- Removal of No preemption condition
  - Resources should not be preempted
- Elimination of circular wait
  - It is necessary to order the resources in such a way that circular waiting will never occur
- The technique to ensure that deadlock should not occur is called as deadlock avoidance
- A state can be called as safe if the system is capable of providing the resources to each and every process up to maximum value
- A state can be called as unsafe if the system is not capable of providing/ allocating resources to the process



# What is a File? what does File management do?

- A file is a collection of data stored on mass storage
- E.g., in disk or on tape
- This data is divided into records (student information)
- Each record contains a number of fields (roll number, name)
- One or more field has a key field (roll number)

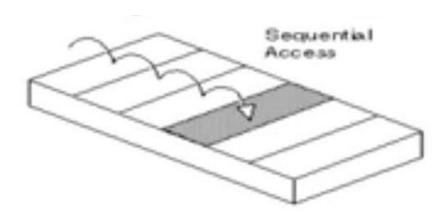
#### Work of File management

- To manage files and folders on hard disk
- · Allocate space to file and folders
- De-Allocate space to file and folders
- · Objectives of file management
  - Easy to fetch data
  - Data can be fetched faster
  - o Operations like insert, delete, or update can be easy
  - o reduces duplicate files
  - minimal cost of storage

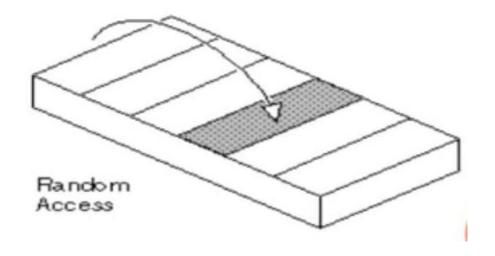


# What is File Organization? or explain all the types of file organization types

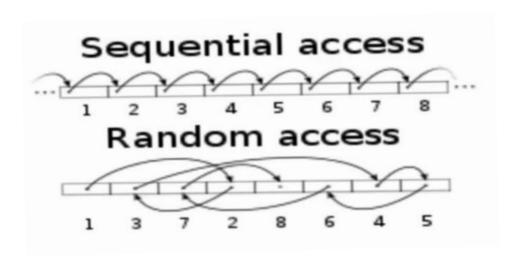
- the technique to arrange the records on the secondary storage is called as file organization
- Sequential Files
  - the records are stored in a sequence
  - sequence means the records are stored one after another
  - the records can be retrieved only in a sequence in which they are stored in
  - access of data is slow
  - if the last record has to be fetched all the records will be read before it



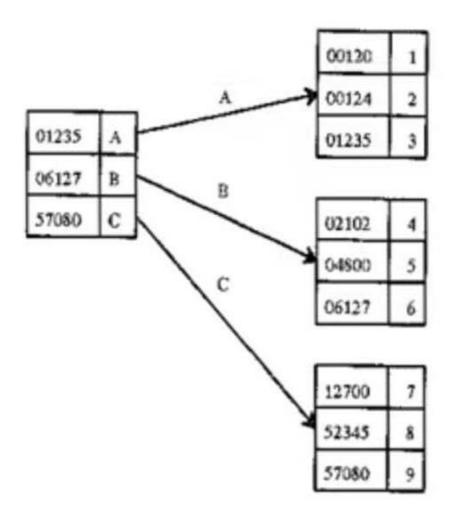
- Direct or random files
  - o the records are not stored in a particular sequence
  - the key of the record is used to locate this file
  - each file is accessed directly without going through all previous files



Random access is to store files in a random format



- Indexed Sequential method
  - The records are stored in an order (ascending or descending)
  - This order is based on the key
  - The index is stored in a separate file
  - This index contains the key values and the corresponding disk address
  - The index file is updated whenever new record is added or deleted from the file
  - This method is slower as compared to direct method
  - It also requires more space





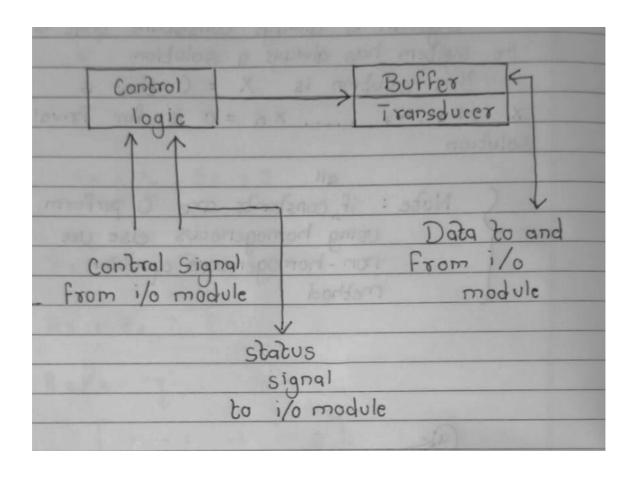
### **Input Output Devices**

- To make a computer system interactive some input devices like keyboard, mouse, scanner, etc, and output devices like printer, plotter are attached to the system
- Input Output Devices can be grouped into the following
- Human readable : used to communicate with the user e.g., mouse, printer
- Machine readable: used to communicate with electronic equipment, eg. sensors, controllers
- Communication : used to communicate with network devices e.g, modems



#### Short note on overview of I/O devices

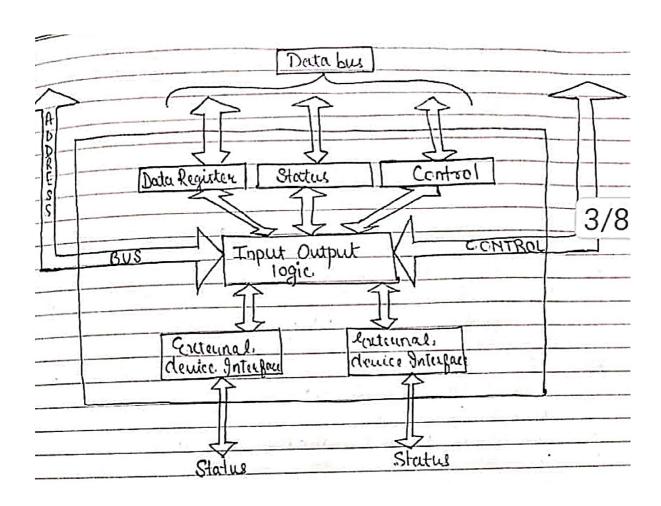
- Input output devices can send and receive data to and from I/O module
- control signal sent by the I/O module decides if the data has to be read from the device or to write to the device
- Status Signal indicates if the device is read to transfer data or if it is busy with some work
- Control logic controls the operation of device in response to the direction form the I/O module
- Transducer converts electrical data to mechanical signal in case of output devices and mechanical to electrical in case of input devices
- Each Input Output device has a piece of memory called as buffer to temporary store data which is transferred





#### Structure of I/O module

- The I/O module connects to the rest of the computer through system bus
- Data Register : the data transferred to and from the module is temporary stored in one or more register
- Status register : status register provides status information
- Control register : it accepts detailed control information from the CPU
- I/O logic : uses control bus to interact with the CPU





### **Explain Programmed I/O or Polling**

- Programmed I/O (Input/Output) is a basic I/O mechanism in computing where the CPU directly controls data transfer between peripheral devices and memory.
- In this method, the CPU initiates each data transfer operation by issuing specific commands to the I/O devices.
- It requires constant CPU involvement, making it inefficient for highspeed data transfer but simple to implement.
- 1. CPU issues Read command to I/O module
- 2. CPU read reads status register of I/O module
- 3. if I/O module is not ready repat step 2
- 4. Else read from I/O module
- 5. Write data into main memory



### **Explain Interrupt Driven I/O**

- Interrupted I/O is an I/O mechanism in computing where the CPU doesn't continuously poll or control data transfer between peripherals and memory.
- Instead, it relies on hardware interrupts to signal when an I/O
  operation is complete. When an I/O device finishes its task, it sends
  an interrupt request to the CPU, which temporarily suspends its
  current task to service the interrupt.
- This approach allows the CPU to handle other tasks while waiting for I/O operations to complete, making it more efficient than programmed I/O for multitasking.



### **Direct Memory Access (DMA)**

- Direct Memory Access (DMA) is an I/O mechanism in computing that enables high-speed data transfers between peripheral devices and memory without involving the CPU extensively.
- DMA controllers take over the data transfer process after the CPU initiates the operation, allowing the CPU to perform other tasks in the meantime.
- This reduces CPU overhead and improves overall system performance, making it especially useful for large data transfers or multitasking environments.



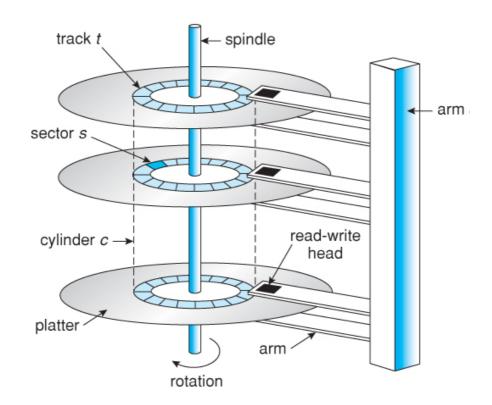
## Why need Disk Scheduling?

- Disk scheduling is done by OS to schedule I/O request arriving for the disk. it is also known as I/O scheduling
- It is important because multiple I/O request may arrive by different processes and only one request can be served at a time by the disk
- thus other request have to wait and needed to be scheduled



#### Write short note on Disc

- Disk is divided into tracks, cylinders and sectors
- Hard disks drives are organized as a concentric stack of disk or platters
- Each platters has 2 surfaces and each surface has 2 read/ write heads
- each platter has the same no of tracks



- Track: Each disk is divided into a number of concentric circle, inner track are more densely packed with data
- · Sector : All the tracks are sub divided into sectors
- Cylinder: A set of corresponding tracks in all the surface is called as cylinder
- The total number of bytes that can be stored in a data pack is = number of cylinders x tracks per cylinder x sectors per track x bytes per sector



#### **Factors affecting the Speed of Disk**

 Their are 2 components that affect the speed of disk that are Access time and Disk bandwidth

#### **Access Time**

- Access Time depends on 2 factors Seek time and Rotational Latency
- Seek Time: Seek time is the time taken to locate the disk arm to a specified location on the track
- The disk scheduling algorithm with least seek time is better
- Rotational Latency (RT): It is the time taken by the disk sector to rotate into a position
- Access Time = Seek time + RT + disk bandwidth

#### **Disk Bandwidth**

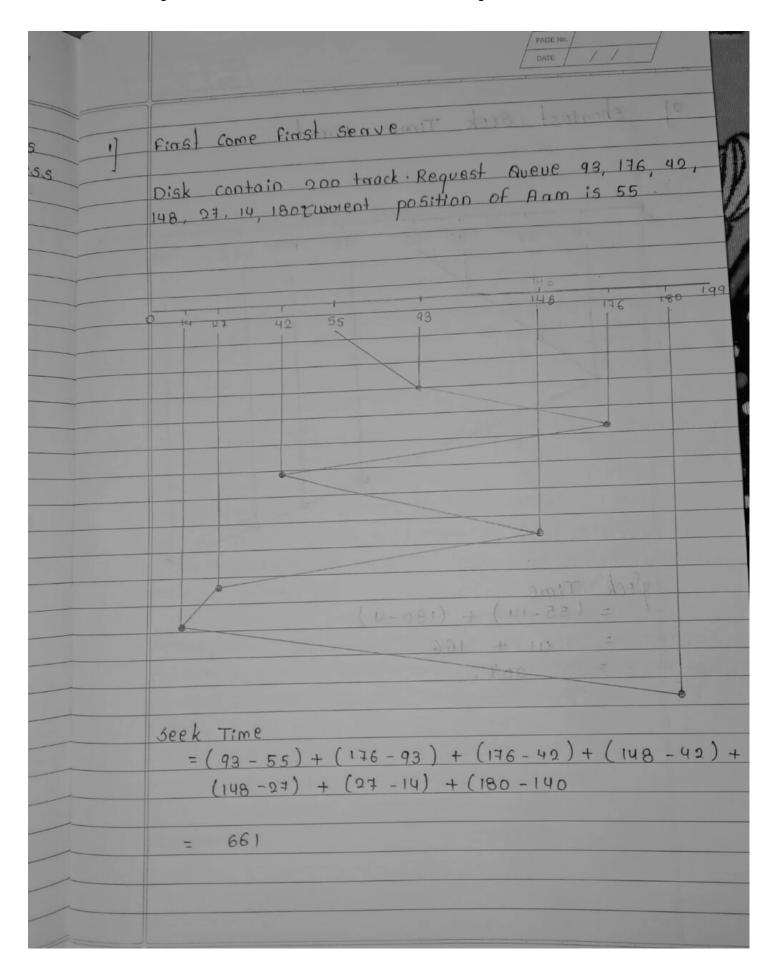
- The bandwidth of the disk is the ration of total number of bytes transferred and total time for this transfer
- Bandwidth depends on the rotating speed



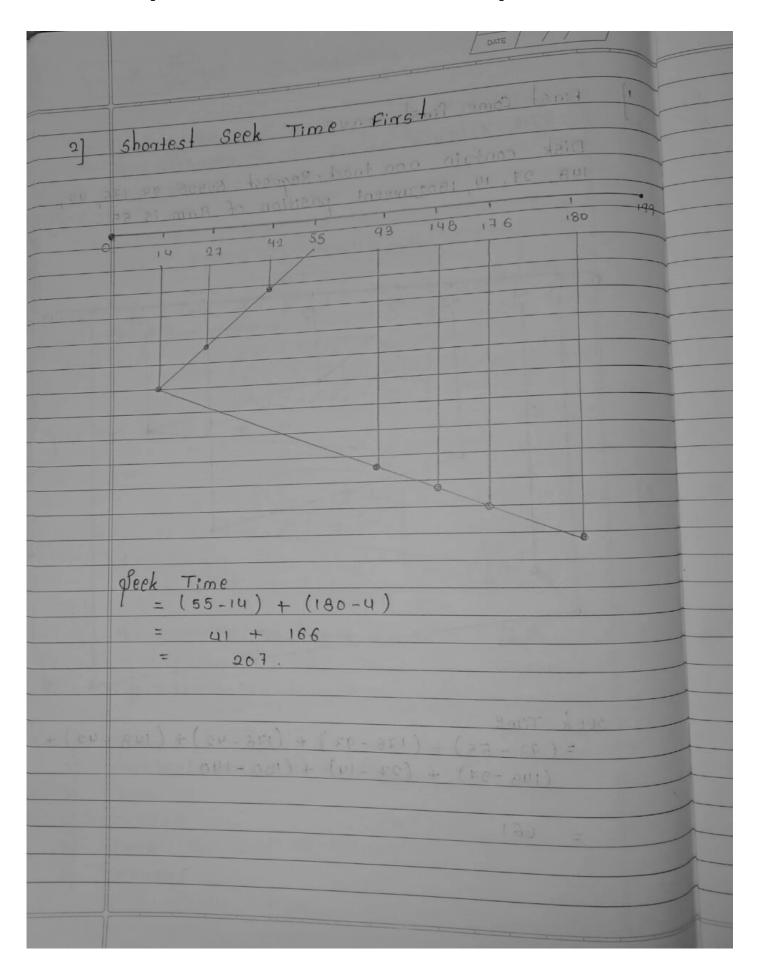
#### **Disk Scheduling Algorithms**

- FCFS: The request are addressed in the order they are arrived in the disk queue
- SSJF: The request with Shortest Seek time are executed first
- SCAN: In Scan algorithm the disk arm moves into a particular direction and serves all the request and after reaching the end of disk it changes the direction ad serves the request in this direction
- C-SCAN: Goes in one direction then to the end of another direction and then goes to the other end performing a circle
- LOOK: is similar to scan but instead of going to the end it goes to the last request and then reverses the direction from there serving all the request in path
- C-LOOK: C-LOOK is similar to C-Scan algorithm, the arm goes to the last request in one direction and from there it goes to the last request at the other end

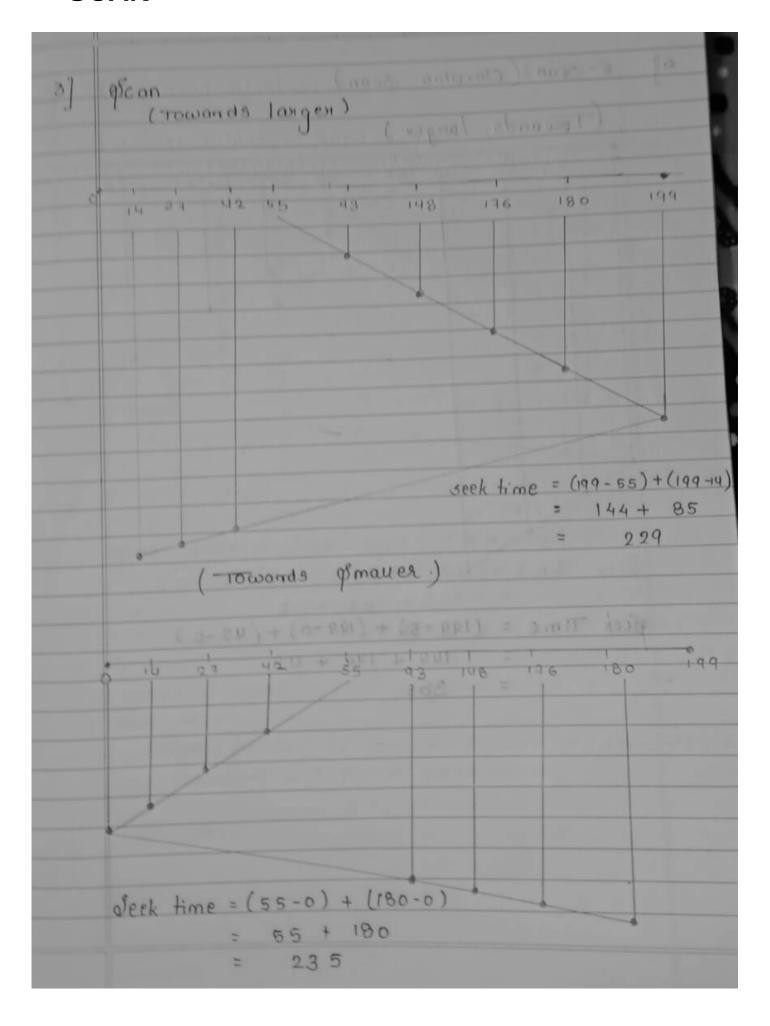
# **FCFS (First Come First Serve)**



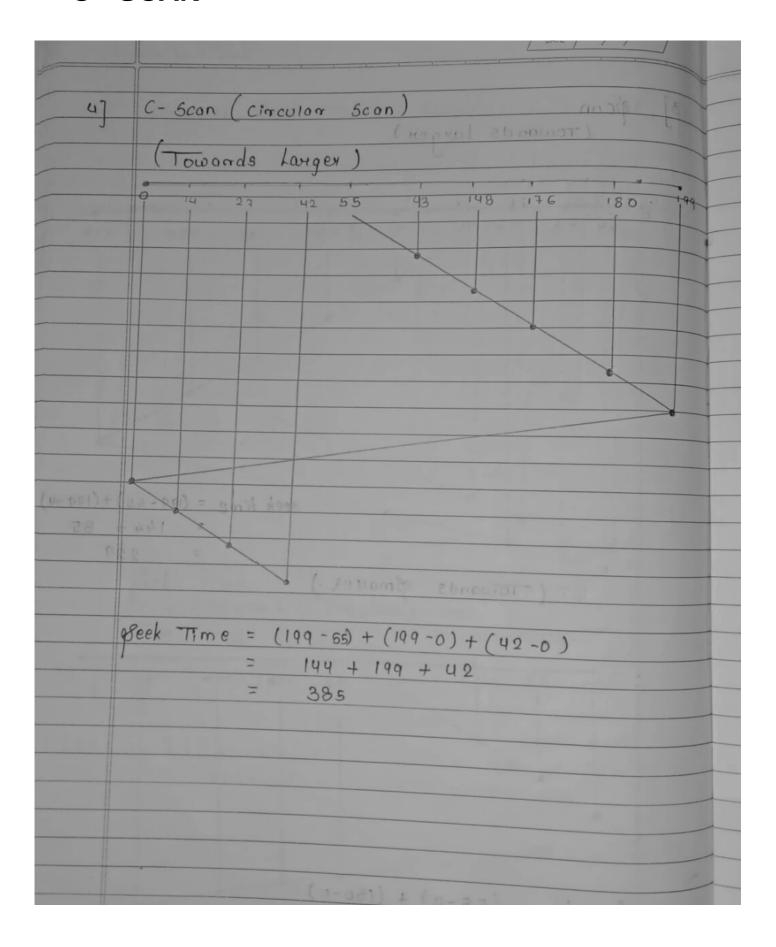
# **SSTF (Shortest Seek Time First)**



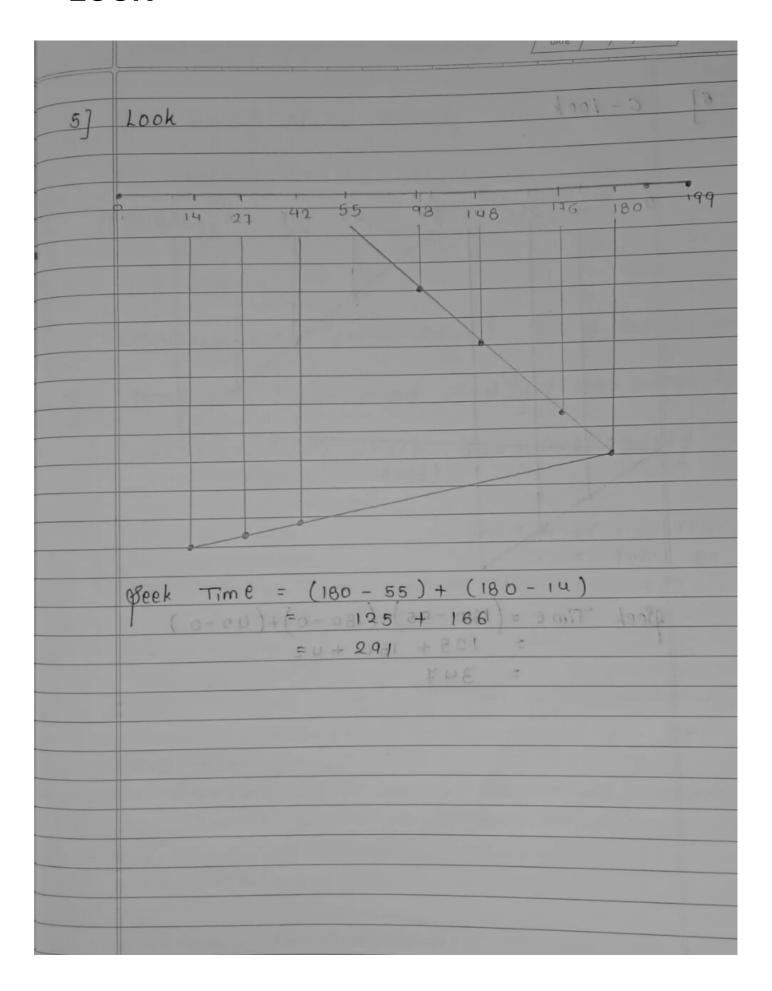
#### **SCAN**



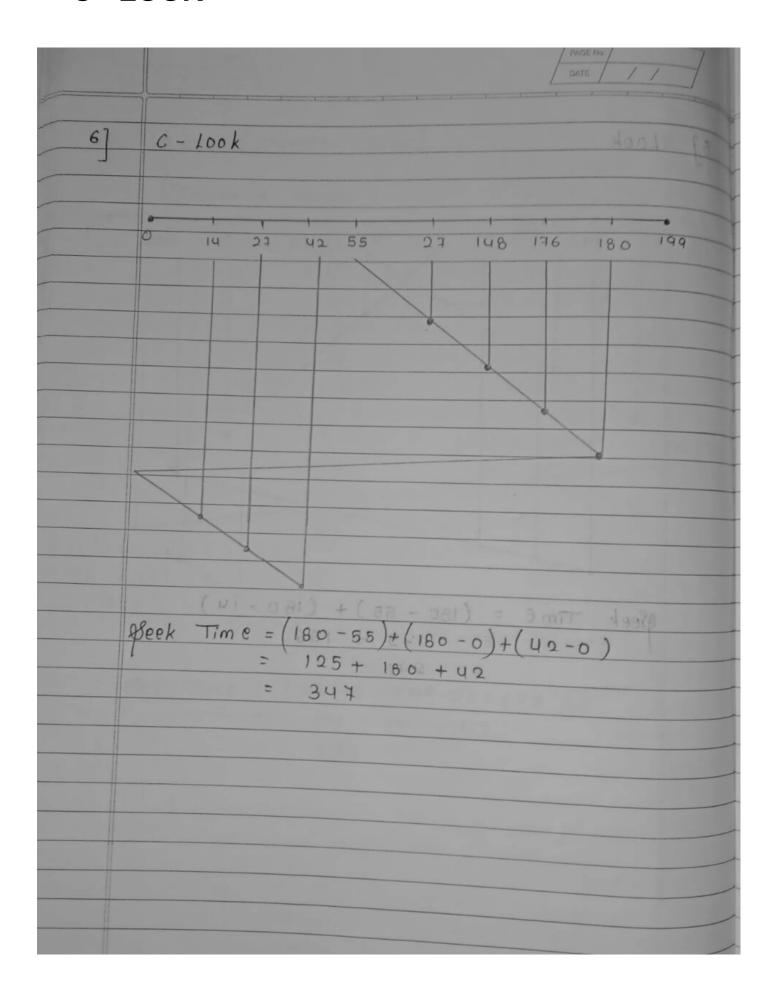
## C - SCAN



### **LOOK**



### C - LOOK





#### **Goals of Protection**

- Computer Consists of a Collection of objects, hardware and software
- Each Object has a unique name and can be accessed through a well defined set of operations
- Protection problem ensure that each object is accessed correctly and only by those processes that are allowed to do so
- Need of protection
  - Prevent the mischievous and intentional violations
  - Improve reliability



### **Security Violation Categories**

- Breach of confidentiality: unauthorized reading of data
- Breach of Integrity : unauthorized modification of data
- Breach of Availability: Unauthorized destruction of data
- Theft of service : Unauthorized use of resources
- Denial of service (DOS): prevention of legitimate use

# THE END